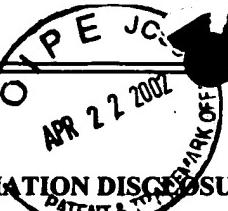


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INFORMATION DISCLOSURE STATEMENT

Applicants: Takeshi FUKADA et al.

Filing Date: July 16, 2001

Group Art Unit: 2825

OTHER DOCUMENT

(Including Author, Title, Date, Patent Pages, Etc.)

Examiner Initial	
<i>J. H. Malan</i>	Inventor: Yasuhiko TAKEMURA, "Process for Fabricating Semiconductor Device" Filing Date: October 26, 2001, Specifications and Drawings for US Application Serial No. 09/983,882

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Examiner <i>J. H. Malan</i>	Date Considered	1-12-03
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